

INTERNATIONAL STANDARD

IEC
60191-6-5

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Mechanical standardization of semiconductor devices –

Part 6-5:

General rules for the preparation of outline drawings of surface mounted semiconductor device packages –

Design guide for fine-pitch ball grid array (FBGA)

Normalisation mécanique des dispositifs à semiconducteurs

Partie 6-5:

*Règles générales pour la préparation des dessins
d'encombrement des dispositifs à semiconducteurs
à montage en surface –*

*Guide de conception pour les boîtiers matriciels à billes
et à pas fins (FBGA)*



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